

(A.Y-2014-15)

TI Sem

PDC

LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
		<u>Unit - I:</u>		CR		
1	2.2.15	Introduction to PDC	I	"		
2	3.2.15	High pass RC circuit as differentiator & sinusoidal response	I	"		
3	4.2.15	High pass RC circuit step response	I	"		
4	5.2.15	Response for pulse & square wave	I	"		
5	7.2.15	Response for ramp & problems	I	"		
6	10.2.15	RC low pass circuit action as integrator & sinusoidal response	I	"		
7	13.2.15	Step & pulse response	I	"		
8	16.2.15	Square wave input & ramp input response	I	"		
9	17.2.15	Alternating				
10	18.2.15	RL & RLC circuits response for step	I	CR		
11	20.2.15	Ringing circuit & problem	I	"		
		<u>Unit - II:</u>	II	"		
12	23.2.15	Diode clippers shunt clippers	II	"		
13	24.2.15	Series clippers	II	"		
14	26.2.15	Two level clippers	II	"		
15	27.2.15	Series & shunt wave clippers	II	"		
16	2.3.15	Transistor clipper & emitter coupled clipper	II	"		
17	3.3.15	Comparator	II	"		
18	4.3.15	negative clamper	II	"		

LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
19	23.3.14	clamping circuit	I	CR		
20	24.3.14	effect of diode characteristics on clamping voltage & peak	I	"		
		Unit - II				
21	25.3.14	In diode switching bias piece-wise linear char	II	CR		
22	27.3.14	Transistor as a switch & switching bias	II	"		
23	30.3.14	breakdown with consideration of its parameter with temp	II	"		
24	31.3.14	Design of transistor switch	II	"		
25	1.4.14	Binary principle & operation	III	"		
26	3.4.14	Analysis of fixed bias binary	III	"		
27	4.4.14	commutating capacitors triggering of binary	III	"		
28	7.4.14	Schmitt trigger	III	"		
29	8.4.14	Analysis of self bias binary & its application	III	"		
30	10.4.14	problems				
		Unit - IV	IV	CR		
31	13.4.14	Basic principle of monostable mult	IV	"		
32	14.4.14	Analysis of collector coupled monostable mult	IV	"		
33	15.4.14	triggering a monostable	IV	"		
34	17.4.14	principle of Astable mult	IV	"		
35	20.4.14	Analysis & design of astable mult	IV	"		
36	21.4.14	problems	IV	"		

LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
37	22.4.18	Generation of sine wave signal	IV	CR		
38	24.4.18	Basic principle of Miller & bootstrap tone generator	IV	"		
39	27.4.18	Transistor Miller tone generator	IV	"		
40	28.4.18	Transistor bootstrap tone generator	IV	"		
41	29.4.18	UJT sweep circuit	IV	"		
		Unit - V:				
42	28.5.18	monostable blocking oscillator (one-shot)	V	CR		
43	29.5.18	emitter tuning	V	"		
44	1.6.18	Antic blocking oscillator drive controlled	V	"		
45	4.6.18	Applications	V	"		
46	5.6.18	principle of sampling gate	V	"		
47	08.6.18	Unidirectional sampling gate	V	"		
48	11.6.18	Bidirectional sampling gate	V	"		
49	12.6.18	reduction of pedestal	V	"		
50	13.6.18	Bidirectional diode sampling gate	V	"		
51	14.6.18	four diode sampling gate	V	"		
52	18.6.18	Applications of sampling gate	V	"		

Page 5/12